

# A WORLD OF JOB OPPORTUNITIES.



<b>Name:</b>	11833
<b>Available for Interview:</b>	Usually after 5:30pm, weekdays
<b>Available from:</b>	Needs 2 months of previous notice
<b>Salary expectation:</b>	65,000 Euro gross per year

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# CURRICULUM VITAE

**Place of residence** Italy

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## Profile

He has been working as Hardware/Electronics Engineer for 6 years in different products and industries. He has experience in electronic manufacturing, microelectronics, aerospace, and defense industries, always working in electronics development. He has extensive experience in all schematics, FPGA, microcontrollers, PCB layout, product specifications, microprocessors, firmware development and product testing with automatic tools. He is very motivated about an opportunity that would improve his firmware and design skills and to develop different kinds of products. In the future, he would like to assume more responsibility and share knowledge with others, by having a leading role and help young Engineers to grow. He is fully available and interested to relocate to another country. He has very good communications skills and can easily express his ideas. He considers to be very reliable and always available to help in the team.

## Skills & Competences

- Project management
- Good communication skills (spoken and written);
- Team player
- Training skills

## Application Software

*Design Tool:*

- High Level: Altium, Mentor XPedition, Orcad
  - Basic Level: Zucken
  
  - High Level: Labview, Matlab, Ansi C, VBA in Excel, VHDL, Xilinx ISE,
  - Basic level: C++, Assembler, System C
  
  - High Level: LTSpice, PSPICE
- Development environment:*
- High level: Windows (XP, Vista, 7) and Office (Word, Excel, PowerPoint)
  - Basic level: Unix, MacOS, Linux
  - MPLAB XIDE

## Education

**2011**

### Masters in Digital Electronics

- Final Year Project: MIM devices for memory application: ReRAM
- Memory Characterization of HfO<sub>2</sub> cells to verify data retention properties, temperature stability and conductive mechanism with different electrodes
- Project was submitted to IEEE IEDM 2011 Washington DC: "Experimental and Theoretical Study of Electrode Effects in HfO<sub>2</sub> based RRAM"

**2009**

### Bachelor of Science in Electronic

## Work experience

### **October 2018 – present Hardware Engineer**

#### **Tasks/responsibilities**

- Hardware Design for Digital Platforms to be implemented in fixed retail scanners, hand handled scanners and system for vision automation.
- Develop quad core microprocessor boards (iMX8M) with LPDDR4, eMMC 5.1 and Physical Ethernet in Mentor Graphics Xpedition.
- Pre and Post Layout Simulation up to 1,5GHz for LPDDR4 Signal integrity with Mentor Hyperlynx
- Motherboard design for powering and testing peripherals: Gigabit ethernet, USB OTG, MIPI-CSI camera interface, SPI, I2C and UART.
- Linux Kernel and u-boot firmware develop for integrating all peripherals.
- Labview Test bench for stressing the system and monitoring current, temperatures and functionalities.
- Validation test for all peripherals and EMI (EN55022) certification

### **November 2017 – October 2018 Hardware Engineer**

#### **Tasks/responsibilities**

- Hardware Design in Zuken for Military Tactical Computer: Carrier Board, power supply, LCD Display, CPU.
- Layout development and 3D implementation in Altium for High speed signal (i.e. USB, PCI Express, LVDS).
- System Integration and test validation for MIL-461 and MIL-810
- EMC/EMI Test for Army Ground compliance
- Firmware developing for Microchip microcontrollers

### **November 2016 – November 2017 Hardware Engineer**

#### **Tasks/responsibilities**

- Hardware Design in OrCad and Altium for Power board, User Interface and lighting for Food Preservation applianceDuty astronomer; maintaining and observing with the telescopes
- Layout development and 3D implementation in Altium
- FMEA analysis for VDE and CE safety approval
- Automatic Test bench in Labview for Power board efficiency performances and temperatures.
- Relay Reliability test development for endurance and mean time before failure

# CURRICULUM VITAE

## **September 2012 – October 2016 Electronic Test Engineer**

### **Tasks/responsibilities**

- Radiation Effect Analysis by heavy ions, protons and  $\gamma$  rays on digital analogic and Radiofrequency devices used in space applications (FPGA, DAC, ADC, OPA, LVR, DC/DC, MOSFET, HEXFET, BJT)
- Radio Frequency Testing for ESA and ASI qualification project on GaAs and GaN X-Band amplifier (QUAGAS and PRAGAN).
- Test Board Design and realization in Altium according to MIL-STD-883 and ESA/SCC 22900 standards.
- Test Program developing in Labview and Matlab for National Instruments PXI and Agilent equipment (Oscilloscope, multimeter, programmable power supply)
- Test Campaign Planning and Organization in:
  - Cyclotron Institute of Texas A&M University
  - Radiation Facility (RADEF) Jyvaskyla (Finland)
  - South National Laboratory (LNS INFN) Catania (Italy)
  - Casaccia Research Centre ENEA Rome (Italy)
- Writing Device Performance Report for the customer (SELEX, Thales Alenia Space, Sitael, DSO, OHB).
- Mapping the focal surface of the 1m RCC telescope, analyzing stellar profiles
- Measurement of the back focal lengths of the RCC for the upcoming refurbishments
- Calculating the conic constants, and modeling of the 1m RCC telescope

## **December 2011 2011 – September 2012 Neural Net Mixed Signal Design**

### **Tasks/responsibilities**

- Logic Function analysis made with Neural Net.
- Simulations in OrCAD PSPICE
- Printed Circuit Board design with analog devices in OrCAD PCB Editor
- Project Functional Test with various equipment (oscilloscope, multimeter, spectrum analyzer)

### **Languages**

- Italian – Native
- English – Fluent (C1 – PET Certificate)

### **Extracurricular experiences**

- Informatic and Technical staff for the "Crai Supermarket"
- Private Lessons in mathematics and physics
- Football School Coach for Tiber Club